1. General description

XC7SH14 is a high-speed Si-gate CMOS device. It provides an inverting buffer function with Schmitt trigger action. This device is capable of transforming slowly changing input signals into sharply defined, jitter-free output signals.

2. Features and benefits

- Symmetrical output impedance
- High noise immunity
- Low power dissipation
- CMOS input levels
- Balanced propagation delays
- ESD protection:
 - HBM JESD22-A114E: exceeds 2000 V
 - MM JESD22-A115-A: exceeds 200 V
 - CDM JESD22-C101C: exceeds 1000 V
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

3. Applications

- Wave and pulse shapers
- Astable multivibrators
- Monostable multivibrators

4. Ordering information

Table 1. Ordering information

Type number	Package							
	Temperature range	Name	Description	Version				
XC7SH14GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1				
XC7SH14GV	-40 °C to +125 °C	SC-74A	plastic surface-mounted package; 5 leads	SOT753				

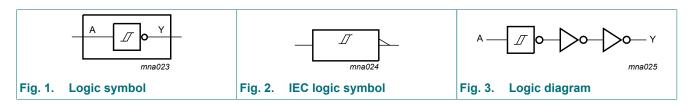
5. Marking

Table 2. Marking codes					
Type number	Marking code [1]				
XC7SH14GW	fF				
XC7SH14GV	f14				

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

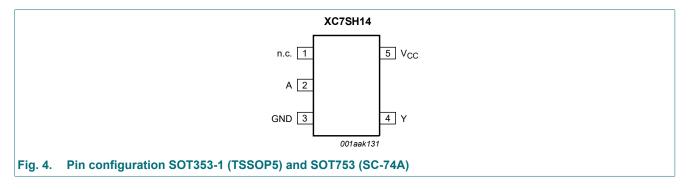
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6. Functional diagram



7. Pinning information

7.1. Pinning



7.2. Pin description

Table 3. Pin description

Symbol	Pin	Description
n.c.	1	not connected
A	2	data input
GND	3	ground (0 V)
Y	4	data output
V _{CC}	5	supply voltage

8. Functional description

Table 4. Function table

H = HIGH voltage level; L = LOW voltage level

Input	Output
A	Y
L	Н
Н	L

9. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CC}	supply voltage			-0.5	+7.0	V
VI	input voltage			-0.5	+7.0	V
I _{IK}	input clamping current	V _I < -0.5 V		-20	-	mA
I _{OK}	output clamping current	$V_{\rm O}$ < -0.5 V or $V_{\rm O}$ > $V_{\rm CC}$ + 0.5 V	[1]	-	±20	mA
I _O	output current	-0.5 V < V _O < V _{CC} + 0.5 V		-	±25	mA
I _{CC}	supply current			-	75	mA
I _{GND}	ground current			-75	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C	[2]	-	250	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT353-1 (TSSOP5) package: Ptot derates linearly with 3.3 mW/K above 74 °C.

For SOT753 (SC-74A) package: Ptot derates linearly with 3.8 mW/K above 85 °C.

10. Recommended operating conditions

Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CC}	supply voltage		2.0	5.0	5.5	V
VI	input voltage		0	-	5.5	V
Vo	output voltage		0	-	V _{CC}	V
T _{amb}	ambient temperature		-40	+25	+125	°C

11. Static characteristics

Table 7. Static characteristics

Voltages are referenced to GND (ground = 0 V).

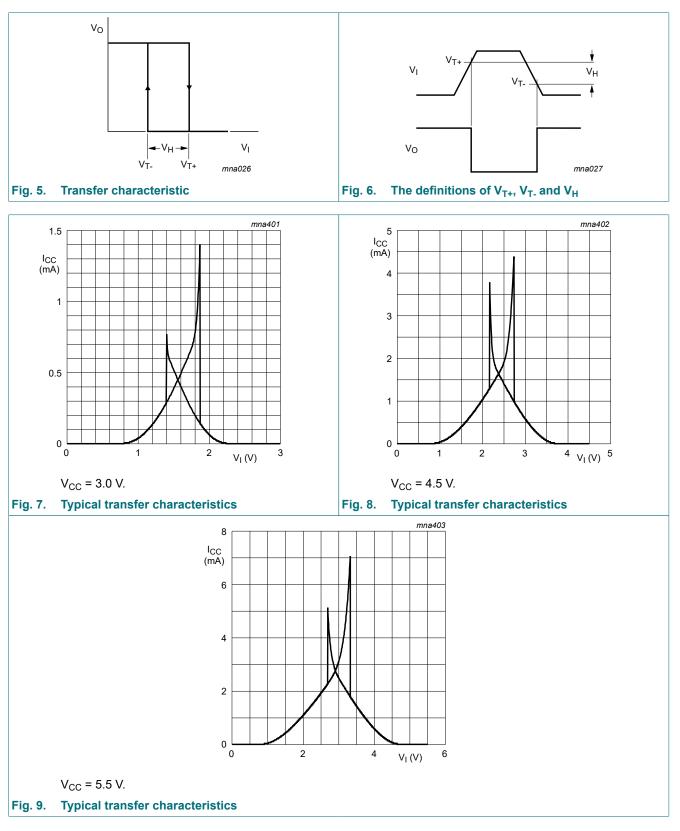
Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	1
V _{OH}	HIGH-level	$V_{I} = V_{T+} \text{ or } V_{T-}$								
	output voltage	I _O = -50 μA; V _{CC} = 2.0 V	1.9	2.0	-	1.9	-	1.9	-	V
		I _O = -50 μA; V _{CC} = 3.0 V	2.9	3.0	-	2.9	-	2.9	-	V
		I _O = -50 μA; V _{CC} = 4.5 V	4.4	4.5	-	4.4	-	4.4	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.58	-	-	2.48	-	2.40	-	V
		I _O = -8.0 mA; V _{CC} = 4.5 V	3.94	-	-	3.8	-	3.70	-	V
V _{OL}	LOW-level output voltage	$V_{I} = V_{T+} \text{ or } V_{T-}$								
		I _O = 50 μA; V _{CC} = 2.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 50 μA; V _{CC} = 3.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 50 μA; V _{CC} = 4.5 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.36	-	0.44	-	0.55	V
		I _O = 8.0 mA; V _{CC} = 4.5 V	-	-	0.36	-	0.44	-	0.55	V
I _I	input leakage current	V _I = 5.5 V or GND; V _{CC} = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μA
I _{CC}	supply current	V _I = V _{CC} or GND; I _O = 0 A; V _{CC} = 5.5 V	-	-	1.0	-	10	-	40	μA
Cı	input capacitance		-	1.5	10	-	10	-	10	pF

12. Transfer characteristics

Table 8. Transfer characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V). See Fig. 5 and Fig. 6.

Symbol	Parameter	Conditions	25 °C			-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	_
V _{T+}	positive-going	V _{CC} = 3.0 V	-	-	2.2	-	2.2	-	2.2	V
	threshold voltage	V _{CC} = 4.5 V	-	-	3.15	-	3.15	-	3.15	V
voitage	V _{CC} = 5.5 V	-	-	3.85	-	3.85	-	3.85	V	
V _{T-}	negative-going	V _{CC} = 3.0 V	0.9	-	-	0.9	-	0.9	-	V
	threshold voltage	V _{CC} = 4.5 V	1.35	-	-	1.35	-	1.35	-	V
	Voltage	V _{CC} = 5.5 V	1.65	-	-	1.65	-	1.65	-	V
	hysteresis voltage	V _{CC} = 3.0 V	0.3	-	1.2	0.3	1.2	0.25	1.2	V
		V _{CC} = 4.5 V	0.4	-	1.4	0.4	1.4	0.35	1.4	V
		V _{CC} = 5.5 V	0.5	-	1.6	0.5	1.6	0.45	1.6	V



12.1. Transfer characteristic waveforms

XC7SH14

13. Dynamic characteristics

Table 9. Dynamic characteristics

GND = 0 V. For waveform see Fig. 10. For test circuit see Fig. 11.

Symbol	Symbol Parameter Conditions			25 °C		-40 °C to +85 °C		-40 °C to +125 °C		Unit	
				Min	Тур	Max	Min	Max	Min	Max	
t _{pd}	propagation	A to Y;	[1]								
	delay	V _{CC} = 3.0 V to 3.6 V	[2]								
		C _L = 15 pF		-	4.2	12.8	1.0	15.0	1.0	16.5	ns
		C _L = 50 pF		-	6.0	16.3	1.0	18.5	1.0	20.5	ns
		V _{CC} = 4.5 V to 5.5 V	[3]								
		C _L = 15 pF		-	3.2	8.6	1.0	10.0	1.0	11.0	ns
		C _L = 50 pF		-	4.6	10.6	1.0	12.0	1.0	13.5	ns
C _{PD}	power dissipation capacitance	per buffer; C _L = 50 pF; f = 1 MHz; V _I = GND to V _{CC}	[4]	-	12	-	-	-	-	-	pF

 t_{pd} is the same as t_{PLH} and t_{PHL} . [1]

Typical values are measured at V_{CC} = 3.3 V. [2]

[3] Typical values are measured at $V_{CC} = 5.0 \text{ V}$. [4] C_{PD} is used to determine the dynamic power dissipation P_D (µW).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i + \sum (C_L \times V_{CC}^2 \times f_o)$ where:

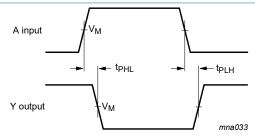
 f_i = input frequency in MHz;

f_o = output frequency in MHz;

C_L = output load capacitance in pF;

V_{CC} = supply voltage in Volts.

13.1. Waveform and test circuit



Measurement points are given in Table 10.

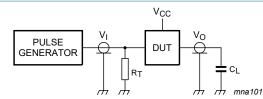
Fig. 10. The input (A) to output (Y) propagation delays

Table 10. Measurement points

Input	Output	
VI	V _M	V _M
GND to V _{CC}	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$

XC7SH14

Inverting Schmitt trigger



Test data is given in Table 11.

Definitions for test circuit:

 C_L = Load capacitance including jig and probe capacitance.

 R_T = Termination resistance should be equal to output impedance Z_o of the pulse generator.

Fig. 11. Test circuit for measuring switching times

Input		Load	Test
V _I	t _r , t _f	CL	
V _{CC}	≤ 3.0 ns	15 pF, 50 pF	t _{PLH} , t _{PHL}

14. Application information

The slow input rise and fall times cause additional power dissipation, which can be calculated using the following formula:

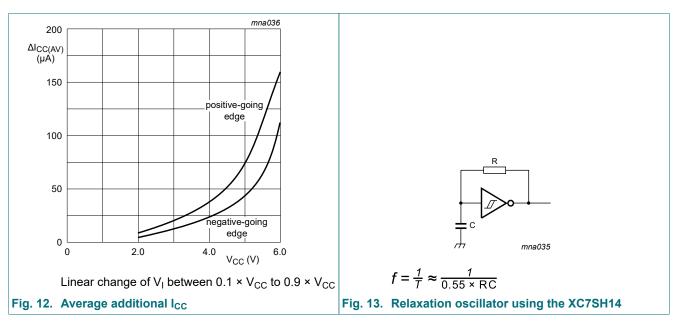
 $P_{add} = f_i \times (t_r \times \Delta I_{CC(AV)} + t_f \times \Delta I_{CC(AV)}) \times V_{CC} \text{ where:}$

- P_{add} = additional power dissipation (µW);
- f_i = input frequency (MHz);
- t_r = input rise time (ns); 10 % to 90 %;
- t_f = input fall time (ns); 90 % to 10 %;
- $\Delta I_{CC(AV)}$ = average additional supply current (µA).

Average additional I_{CC} differs with positive or negative input transitions, as shown in Fig. 12.

For XC7SH14 used in relaxation oscillator circuit, see Fig. 13.

Note to the application information: All values given are typical unless otherwise specified.



15. Package outline

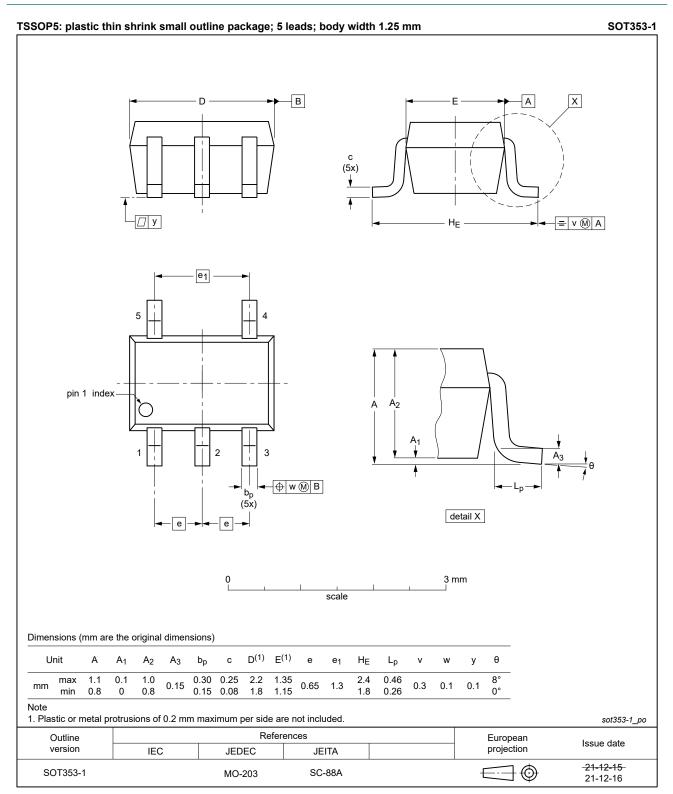


Fig. 14. Package outline SOT353-1 (TSSOP5)

XC7SH14

Inverting Schmitt trigger



SOT753

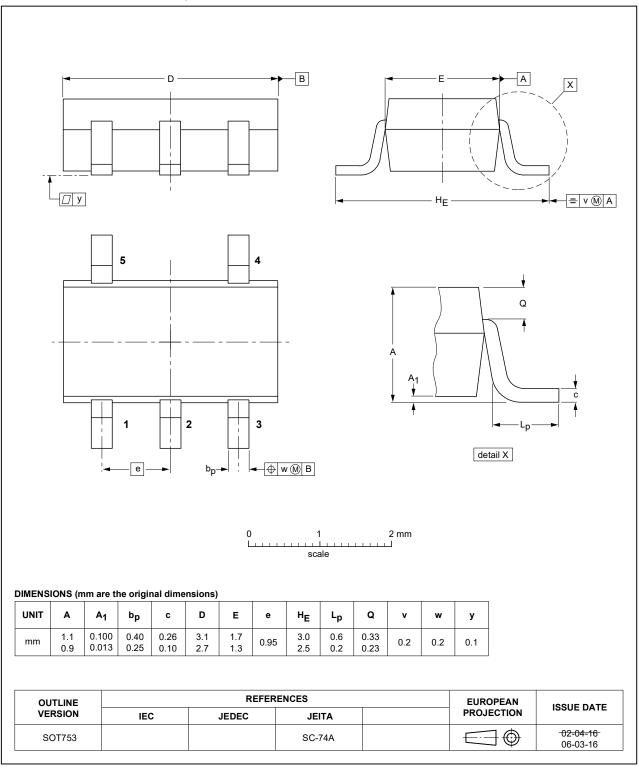


Fig. 15. Package outline SOT753 (SC-74A)

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16. Abbreviations

Acronym	Description
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model

17. Revision history

Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
XC7SH14 v.2	20220124	Product data sheet	-	XC7SH14 v.1	
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. <u>Section 9</u>: Derating values for P_{tot} total power dissipation updated. <u>Fig. 14</u>: Package outline drawing SOT353-1 (TSSOP5) has changed. 				
XC7SH14 v.1	20090901	Product data sheet	-	-	

18. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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